

**Time Table for Fourth Semester B. Tech Degree Practical Examination
October 2020 (2013 Scheme) BRANCH: Electronics & Communication Engineering
13.407- Digital Integrated Circuits Lab, 13.408-Analog Integrated Circuits Lab**

Subject Code	Subject	Course	Course Code	College Code	Colleges registered for Lab Examination	Centre of Examination	Date & Time of Examination
13.407	Digital Integrated Circuits Lab	Electronics & Communication Engineering	412	402	SCT College of Engineering, Pappanamcode	Govt. Engineering College, Barton Hill, TVPM	16/03/2021 9.30am To 12.30 Pm
				404	University College of Engineering, Kariyavattom		
13.408	Analog Integrated Circuits Lab	Electronics & Communication Engineering	412	401	Govt. Engineering College, Barton Hill	Govt. Engineering College, Barton Hill, TVPM	17/03/2021 9.30am To 12.30Pm
				402	SCT College of Engineering, Pappanamcode		
				404	University College of Engineering, Kariyavattom		

Sd/-
Controller of Examinations